

WINTeCHCON - 2018

Women's Technical Conference

A One-day conference ... 28 September 2018 ... 9AM to 5PM

Spring Hall, HOTEL ROYAL ORCHID, Old Airport Road, Bangalore

Objective: To provide opportunities to women technology leaders from India to present their work in emerging knowledge areas

Theme: Designing the Future Electronic Systems & Applications



Organized by: IEEE CAS Bangalore Chapter, In cooperation with IEEE Bangalore Section and IEEE WIE Council (IEEE Women in Engineering), Bangalore



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7.45am – 8.45am	Registration	
8.45am – 9.00am	Inauguration and Welcome Address	
9.00am – 9.30am	Keynote : How AI is Changing the World of IoT Shalini Kapoor, Distinguished Engineer, Director & CTO Watson IoT and Watson Education, IBM	
9.30am – 10.00am	Coffee Break	
	Session 1.1: Vehicular Electronics and Safety	Session 2.1: Wearables
10.00am – 11.00am	Paper 12 Design Approach for CCSDS Complied Command Operation and Frame Operation Procedure for Integrated Spacecraft Checkout <i>Parul Parul, Sangeetha K. and Vithal Metri (ISRO)</i>	Paper 93 Short-term HRV using acceleration PPG under severe ambient settings using in-house developed wearable <i>Payal Mohapatra, Preejith Sp and Mohanasankar Sivaprakasam (Indian Institute of Technology, Madras)</i>
	Paper 73 Information Extraction for Lightning Strike Related Aircraft Maintenance <i>Ankita Mathur, Halasya Siva Subramania and Micah Goldade (Boeing)</i>	Paper 47 Indoor positioning using cellular network and relay node for wearables <i>Sreelakshmi Gollapudi, Lalit Kumar Pathak, Tushar Vrind, Diwakar Sharma and Samir Kumar Mishra (Samsung Semiconductor India R&D, Samsung Electronics)</i>
	Paper 77 Functional safety in multicore heterogeneous systems for automotive surround view applications <i>Vibha Pant, Piyali Goswami and Sujith Shivalingappa (Texas Instruments, Bangalore)</i>	Paper 27 Dimensionality Reduction for Handwritten Digit Recognition <i>Ankita Das, Tuhin Kundu and Chandran Saravanan (Jalpaiguri Government Engineering College)</i>
	Session 1.2: VLSI Design and Test	Session 2.2: System Optimization
11.00am – 12.00pm	Paper 18 Unified Verification Methodology for Digital and Analog Mixed-Signal Co-Simulation for Low Power Embedded SoC <i>Ashwini Padoor and Lakshmanan Balasubramanian (Texas Instruments)</i>	Session 2.2 : Paper 79 Dual-Purpose Hardware Accelerator to implement a High-throughput FFT and Sorting Engine <i>Indu Prathapan and Pankaj Gupta (Texas Instruments)</i>
	Paper 44 Design for Test Techniques to Enable Automotive Functional Safety <i>Swathi Gangasani, Prasanth V and Maheedhar Jalasutram (Texas Instruments)</i>	Paper 94 Dynamic Analytical Modeling and Optimization of Critical Healthcare Data using Connectionism Systems <i>Gagana B, H A Ujjwal Athri and Dr. Natarajan S (PES University)</i>
	Paper 83 Divergence Engine: Early prediction of clock-tree divergence at Logic-synthesis stage	Paper 97 Improved Network Analytics with novel feedback quantities for Self Optimized Networks <i>Roshni Chatterjee (Samsung Semiconductor India R&D)</i>

	<i>Sanjana Sundaresh, Murali Mohan Thota and Atul Garg (Texas Instruments)</i>	
12.00pm – 1.15pm	Lunch Break and time to visit the posters and demos	
1.15pm – 1.45pm	Keynote Quantum Computing – Benefits and Challenges Susmita Sur-kolay, Advanced Computing & Microelectronics Unit, Indian Statistical Institute, Calcutta	
1.45pm – 3.00pm	Tutorial 1: Functional Safety Trends: Challenges and Solutions <i>Trupti Joshi (Intel), Ritu Agarwal (Intel) and Jaya Singh (Texas Instruments)</i>	Tutorial 2: Using Cognitive Technologies for Improved Customer Experience <i>Gargi B Dasgupta (IBM)</i>
3.00pm – 3.45pm	Coffee Break and time to visit demos and posters	
3.45pm – 4.45pm	Panel Discussion “Tracing the Trajectory of Women’s Careers in Technology: Challenges and Solutions’ Moderator: Antaash Sheik (TI). Panelists: Roopashree HM (TI), Krishna Paul (Intel), Viji Ranganna (Qualcomm), Garima Srivastava (Samsung)	
4.45pm – 5.00pm	Award & Closing Ceremony	