



WINTeCHCON 2019

Technical Conference by Women Engineers

Future Technologies for a Smart, Safe & Sustainable World

27th September, 2019 | Sheraton Grand, Whitefield, Bengaluru

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7:45 am - 8:45 am	Registration		
8:45 am - 9:00 am	Inauguration and Welcome Address Venue: Scarlet Hall		
9:00 am - 9:45 am	Keynote I: Dr. Geetha Manjunath, Co-founder, CEO & CTO, NIRAMAI "AI-enabled Healthcare"		
9:45 am - 10:30 am	Coffee Break		
	Track 1 Venue: Scarlet Hall-1 Session 1.1: VLSI Design and Test Session Chair: Rittu Singh, Texas Instruments	Track 2 Venue: Scarlet Hall-2 Session 2.1: System Optimization Session Chair: Manoj Choudhary, Samsung	Track 3 Venue: Scarlet Hall-3 Session 3.1: System Software and AI Session Chair: Raka Singh, Analog Devices
10:30 am - 10:50 am	"Optimized Battery Life for Ultra-Low Power Products" – Jasdeep Jain, Shweta Aladakatti, Shubham Singh, Narayanan Natarajan, Purna Nayak and Yojak Raote (Paper 46, Intel)	"Novel Pulse Width Insensitive Design and Verification Methods" – Ruchi Shankar, Shalini Eswaran, Sharavathi Bhat and Lakshmanan Balasubramanian (Paper 146, Texas Instruments)	"Automatic Defect Classification and Localization of MURA Defects" – Ramya Bagavath Singh, Gaurav Kumar, Priya Ranjan Sinha, Gaurav Sultania, Shashank Shrikant Agashe and Chulmo Kang (Paper 131, Samsung)
10:50 am - 11:10 am	"Validation Methodology for Nest Memory Management Unit" – Nandhini Rajaiah, Jayakumar N Sankarannair and Larry S Leitner (Paper 62, IBM)	"Novel Infrastructure Design for Multi-FPGA Prototyping system" – Vandana Singh, Vani V, Ranjith Kulai and Gregory Matthew James (Paper 34, Intel)	"Uplink Data Compression for Futuristic Wireless Networks" – Shalini Govil, Rohit Kumar and Pavan Kumar Devarayanigari (Paper 133, Samsung)
11:10 am - 11:30 am	"Design and Verification Challenges with Third Party IP Cores" – Ruchi Shankar, Prachi Mishra, Abhinav Parashar, Ashwini Padoor and Lakshmanan Balasubramanian (Paper 117, Texas Instruments)	"Integrating Functional Safety in AC/Servo Drives with Redundancy and Diagnostic Coverage" – Aishwarya Bhatnagar, Vaibhavi Shanbhag and Navaneeth Kumar (Paper 118)	"Edge Acceleration of Computer Vision and Deep Learning Algorithms using OpenCL" – Bakshree Mishra, Dipam Chakraborty, Srajudheen Makkadayil, Saurabh Patil and Bhaskar Nallani (Paper 165, Intel)
	Track 1 Venue: Scarlet Hall-1 Session 1.2: VLSI Design and Test Session Chair: Garima Srivastava, Samsung	Track 2 Venue: Scarlet Hall-2 Session 2.2: System Optimization Session Chair: Saritha Vinod, IBM	Track 3 Venue: Scarlet Hall-3 Session 3.2 System Software and AI Session Chair: Deepti Varadarajan, Mediatek
11:30 am - 11:50 am	"Automation of Verification for Application Specific Instruction Set Processors" – Chaithanya Kolikipudi, Sreenivas Machavaram, Venkataraghavan Parakalan and Milea Tal (Paper 69, Intel)	"Cross-Layer Optimized MBMS Performance for DSDS User Equipment" – Krishna Daamini Ellendula, Vinay Kumar Shrivastava, Lalit Kumar Pathak and Rohan Raj (Paper 136, Samsung)	"An Optimal Resource Sharing Algorithm for Multi-Sim UE with Single RF Subsystem" – Ruchi Kansara, Jitendra Otmani, Niladri Paria and Dr. Sajal Das (Paper 4, Intel)
11:50 am - 12:10 pm	"HVDM Solution for Random Verification of Microcontroller based Subsystems" – Ruchita Pathak, Vishal Kumar Dewan and Aravind Bhat (Paper 74, Intel)	"Novel Approach for Prioritization of TCP Acknowledgements Beyond 4G and 5G Network" – Lakshmi Prasanna Jasti, Rohit Kumar, Tushar Vrind and Lalit Pathak (Paper 138, Samsung)	"Enhancing eMMC with Multi-Stream" – Sushma Vishwakarma, Anantha Sharma and Sharath Kodase (Paper 75, Samsung)
12:10 pm - 12:30 pm	"A Generic Layout Automation Model to Achieve Minimal Time-to-Market" – G S Aishwarya Meghana, Sheetal Y Kochrekar and Poornima Venkatasubramanian (Paper 104, Samsung)	"Kernelization of Vertex Cover Based on Crown Structure" – Divya U (Paper 167, Intel)	"Storage Offload on SmartNICs" – Purna Chandra Mandal, Nayana Mariyappa, Souryendu Das and Anbuvelu Venkataraman (Paper 121, Intel)
12:30 pm - 1:30 pm	Lunch & Learn: Poster, Demo & Hackathon Showcase		

1:30 pm - 2:15 pm	Track 1 Venue: Scarlet Hall-1 Tutorial 1: A Panoramic View of Automated Driving – from ADAS to Driverless Cars – Prabhavathy S Adavikolanu (Intel) and Piyali Goswami (Texas Instruments) Session Chair: Mathangi Raghuraman, Texas Instruments	Track 2 Venue: Scarlet Hall-2 Tutorial 2: Open Source Technique for Large Deep Learning Model Training – Saritha Vinod (IBM) Session Chair: Rugmini Sud, Analog Devices	Track 2 Venue: Scarlet Hall-3 Tutorial 3: Emerging Trends in WiFi – Chandrakalavathi Yellampalli (Samsung) and Pragya Gupta (Samsung) Session Chair: Garima Srivastava, Samsung
2:15 pm - 3:00 pm	Coffee & Learn: Poster and Demo Sessions		
3:00 pm - 3:45 pm	Keynote II: Reena Dayal Yadav, Director, Microsoft Garage India “Journey of Intrapreneurship” Venue: Scarlet Hall		
3:45 pm - 4:45 pm	Panel Discussion: “What Drives Me – Strategies and Tactics for a Technologist’s Career” Moderator: Roopashree HM (Texas Instruments) Panelists: Jiji Jayadevan (Mediatek), Seema Chopra (Boeing), Piyali Goswami (Texas Instruments), Upma Sharda (Qualcomm), Shyama Venugopal (IBM)		
4:45 pm - 5:30 pm	Awards & Closing Ceremony		

Posters:

- Paper 89: “CFS Performance Improvement with Fibonacci Heap” - Saurbhi Agarwal and Alim Akhtar, **Samsung Semiconductor India R&D Center**
- Paper 51: “RapidNet IP Protocol Based Electronic Shelf Label (ESL) Solution” – **Kaveri Banakar** and Shruthi Shetty, **Analog Devices**
- Paper 82: “Integrated Cloud Cockpit: Making Surveillance and Maintenance of Oil Pipelines Practicable” – **Garima Srivastava**, Yeshwant More and Jenifer Sam, **SAP Labs India**
- Paper 84: “ART for Optimized Repeater Flop Network in Hard IP Design” – **Shweta Sharma**, Rahul S Bhatt and Shyam A, **Intel**
- Paper 86: “CCSDS Ground Software Testing Using On-board Telecommand-telemetry Simulator” – **Parul Sharma**, Soumya Shubra Banerjee, Mukul Mukul, Sangeetha K and Vithal Metri, **Indian Space Research Organization**
- Paper 80: “An Automated and Scalable Flow for Multi-Partition RTL Generation” – **Shruti Narake**, Karthikeya Abhiram Peddiraju, Rajkumar Satkuri and Alok Anand, **Intel**
- Paper 105: “Latch Based Designs: Advantages, Challenges and Solutions” – **Neelam Maniar**, Vasudev Anand B, Shikha Subudhi, Hitesh Khandla and Gopalakrishnan Sadagopan, **Intel**
- Paper 109: “Welkin – Parenting App and Analytics Engine Using an IoT Framework” – Heenu Gupta, **Swadha Pathak** and Tushar Vrind, **Samsung Semiconductor India R&D Center**
- Paper 112: “Multi-temporal End-to-end CNN: Audio-scene Classification, Speech Emotion” – **Pushpa Ramu**, T Vijaya Kumar, R Shunmuga Sundar, Anzar Zulfiqar, Rajeev Ranjan, Prashanth Narayan Jalli, Tilak Purohit and V Ramasubramanian, **Samsung Semiconductor India R&D Center**
- Paper 130: “Methodology for Logic Optimization Post Synthesis” – **Sarala Gumma**, Syed M Md Zaid Makandar and Neelam Maniar C, **Intel**
- Paper 135: “Method for Power Conservation for VoNR Devices” – **Kashmira Kapoor**, Dandra Prasad Basavaraj, Shrinath Ramamoorthy and Tushar Vrind, **Samsung Semiconductor India R&D Center**
- Paper 143: “MediaStratify – Optimal Paradigm in Multimedia Cloud Computing” – **Sreelakshmi Gollapudi**, Kartikeshwar Rath, Tushar Vrind, Deepak T Y and Prasad R Rao, **Samsung Semiconductor India R&D Center**
- Paper 158: “Cheetah: Innovation of Design System Infrastructure” – **Divya Ramarao**, Mahesh Deshpande, Susmita Pal and Prateeksha Keshari, **Intel**
- Paper 164: “Advanced Radio Link Manager to Support Mission Critical Services in 5G” – **Sree Lekshmi S** and Sessaiah Ponnekanti, **Amrita Center for Wireless Networks & Applications**

Demos:

- Paper 21: “Automotive Audio Bus®: Green Cars for a Greener Tomorrow” – **Suhasini Raghuram**, Prasanna Thirumaleshwara and Karthik Radhakrishna, **Analog Devices**
- Paper 24: “Workforce Analytics with Career progression constraints (WAC)” – **Sampoorna Hegde**, **IBM**
- Paper 30: “Real-time Audio Tuning Tool for In-cabin Applications” – **Krithika M S**, Divya Venkatesh, John Joseph and Mohan Karthik P, **Analog Devices**
- Paper 38: “Automated QA for Cross Platform” – **Srima Sekaran**, Anand Mutagond and Krishna Gopalakrishnan, **Analog Devices**
- Paper 52: “RapidNet IP protocol based Electronic Shelf Label (ESL) Application” – **Kaveri Banakar** and Shruthi Shetty, **Analog Devices**
- Paper 85: “In field Testing Solution for Latent Fault Coverage” – **Sushma Katti**, **Intel**
- Paper 120: “An Introduction to Autonomous Driving and State Estimation Using a Prototype” – **Saranya Das** and Sriram Madavswamy, **Analog Devices**
- Paper 123: “Solar Tracking Using Thermoelectric Generator” – **Ananya S Bharadwaj**, Prarthana Aithal, Sneha S, Divya B and Nagaraja Rao, **Ramaiah University of Applied Sciences, Bangalore**
- Paper 172: “Interoperable IoT Solution for Smart Environments” – **Ritu Garg**, Aiswarya Cyriac, Kalpana Mandapati, **Intel**